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(54) Title: PHOTOMULTIPLIER TUBE CIRCUIT

(57) Abstract

A photomultiplier tube circuit with reduced power consumption comprising a photomultiplier tube having a plurality of dynodes, charging circuitry for providing charge to the plurality of dynodes and an oscillator for providing a high voltage supply to the charging circuitry characterised in that the photomultiplier tube circuit further comprises means for sampling the voltage of at least one of the dynodes and a switching means for switching the oscillator on and off with respect to the at least one dynode voltage sampled.

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